

Serial No. 10/028,686Docket No. 630-1292P

Group Art Unit 2816

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ABSTRACT OF THE DISCLOSURE

A charge pump device for supplying a boosted voltage to a memory device includes a charge pump part constructed with first to nth unit charge pumps, and a multi-level detector for detecting a level of a boosted voltage to selectively drive the unit charge pumps in accordance with an amount of power consumption of the host and thereby outputting at least one level detection signal.